

S/N 09/820898



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Girish P. Ramanathan et al.

Examiner: Thuan Du

Serial No.: 09/820898

Group Art Unit: 2116

Filed: March 30, 2001

Docket No.: 884.963US1

Title: SYSTEM FOR VARYING TIMING BETWEEN SOURCE AND DATA  
SIGNALS IN A SOURCE SYNCHRONOUS INTERFACE

Assignee: Intel Corporation

Customer No: 21186

2116  
#  
#8/A  
pay  
6-16-04

**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**

JUN 07 2004

Technology Center 2100

This responds to the Office Action mailed on March 25, 2004. Please amend the above-identified patent application as follows.

06/01/2004 CNGUYEN 00000065 190743 09820898

01 FC:1201 172.00 DA